

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,762,450 B2
DATED : July 13, 2004
INVENTOR(S) : Gurtej Sandhu et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

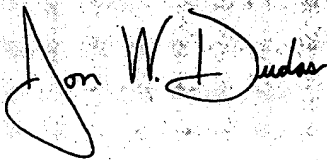
Item [56], References Cited, OTHER PUBLICATIONS, in the "Institute of Semiconductor Physics" reference, please delete "Temmter, D" before "Multilayer Vertical" and insert Temmler, D., --, in the "University of California" reference, please delete "Nierdef, H.," before "Organometallic Chemical" and insert -- Niemer, H., --, in the "IEDM" reference, please delete "H. Wannabe et al.," before "Stacked Capacitor" and insert -- H. Watanabe et al., --.

Column 5,

Line 56, please delete "is" after "layer 34".

Signed and Sealed this

Seventh Day of June, 2005

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area that appears to be a stamp or a designated space for a signature.

JON W. DUDAS

Director of the United States Patent and Trademark Office